



Atty. Dkt. No. 065686-0158

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Takashi SHIMIZU et al.  
Title: CIRCUIT SIMULATION  
APPARATUS  
INCORPORATING DIFFUSION  
LENGTH DEPENDENCE OF  
TRANSISTORS AND METHOD  
FOR CREATING TRANSISTOR  
MODEL


Appl. No.: 10/668,974

Filing Date: 9/24/2003

Examiner: Jacob, Mary C.

Art Unit: 2123

Confirmation Number: 1467

<b>CERTIFICATE OF EXPRESS MAILING</b>	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
EV 830407395 US	October 20, 2006
(Express Mail Label Number)	(Date of Deposit)
Ruthie Vallejo	
(Printed Name)	
	
(Signature)	

**AMENDMENT AND REPLY UNDER 37 CFR 1.111**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Non-Final Office Action dated July 12, 2006, concerning the above-referenced patent application.

**Amendments to the Specification** begin on page 2 of this document. **Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this document.

**Remarks/Arguments** begin on page 6 of this document.

Please amend the application as follows: